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PATENT
Conf. No.: 5349

IN THE UNITED STATES PATENT OFFICE

Applicants: Eric D. Groen et al.

Assignee: Xilinx, Inc.

Title: MGT/FPGA CLOCK MANAGEMENT SYSTEM

Serial No.: 10/660,254

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Examiner: File, Erin M.

Art Unit: 2611

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Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Examiner:

In response to the First Office Action mailed from the Patent Office on October 4, 2006, please replace/substitute the following claims as indicated.

Amendments to the claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.